

HENND 2017 Workshop Posters

Lotte Hotel City Center, Seoul, Korea

October 20, 2017

1	A High-Performance and Energy-Efficient Multi-FPGA Accelerator for Deep Belief Networks	
	Yuming Cheng, Chao Wang, Yangyang Zhao, Xuehai Zhou, and Xi Li	University of Science and Technology of China
2	A Regularization Approach for Quantized Neural Networks	
	Kazutoshi Hirose, Ryota Uematsu, Kota Ando, Kodai Ueyoshi, Masayuki Ikebe, Tetsuya Asai, Masato Motomura, and Shinya Takamaeda-Yamazaki	Hokkaido University
3	Dynamic Fixed-Point Design of Neuromorphic Computing Systems	
	Yongshin Kang and Jaeyong Chung	Incheon National University
4	Streamlined Deployment for Quantized Neural Networks	
	Yaman Umuroglu and Magnus Jahre	Norwegian University of Science and Technology
5	Systematic Design of Approximate Array Multipliers with Different Accuracy	
	Takahiro Yamamoto, Hiroyuki Tomiyama, Ittetsu Taniguchi, *Shigeru Yamashita, and **Yuko Hara-Azumi	Ritsumeikan Univeristy, *Osaka University, **Tokyo Institute of Technology
6	YOLO implementation on ARM Mali GPU	
	Hyungshin Kim	Chungnam University
7	A Fully-Featured FPGA-based Scalable Neuromorphic Simulation Accelerator	
	Gwangmu Lee, Sunghwa Lee, Youngsok Kim, Dongup Kwon, Eunjin Baek, and Jangwoo Kim	Seoul National University
8	Cappuccino: Efficient Inference Software Synthesis for Mobile System-on-Chips	
	Mohammad Motamedi, Daniel Fong, and Soheil Ghiasi	University of California, Davis
9	Analysis of hardware resources in distributed learning	
	Hyungshin Kim	Chungnam University
10	CBinfer: Exploiting Limited Frame-to-Frame Changes for Convolutional Network Inference	
	Lukas Cavigelli, Philippe Degen, Luca Benini	ETH Zurich, Switzerland
11	Filter-Based Deep-Compression with Global Average Pooling for Convolutional Networks	
	Ting-Yun Hsiao and Ching-Te Chiu	National Tsinghua University (NTHU)
12	A Gift from Knowledge Distillation: Fast Optimization, Network Minimization and Transfer Learning	
	Junmo Kim	KAIST
13	Maximizing System Performance by Balancing Computation Loads in LSTM Accelerators	
	Junki Park, Jaeha Kung, Wooseok Yi, and Jae-Joon Kim	POSTECH

14	Reconfigurable Digital Neuromorphic Hardware with Efficient Synapse Memory Structure	
	Jinseok Kim, Jongeun Koo, Taesu Kim, Jae-Joon Kim	POSTECH
15	Performance Analysis of High-Level Synthesis for Neural Network Applications	
	Changsu Kim, Bongjun Hyun, and Hanjun Kim	POSTECH
16	Accelerator-Centric Systems for Scalable and Energy-Efficient Deep Learning	
	Minsoo Rhu	POSTECH
17	Accelerating Convolutional Neural Networks on Smartphones	
	Yongdeok Kim	Samsung Electronics
18	Real-time hardware for Super resolution with Efficient Sub-pixel Very Deep Convolution Network	
	Donghyeon Lee, Ho Seong Lee, Sangheon Lee, *Jin-Sung Kim, *Kyujoong Lee, and Hyuk-Jae Lee	Seoul National University, *Sunmun University
19	Weighted Entropy-based Quantization for Deep Neural Networks	
	Eunhyeok Park and Sungjoo Yoo	Seoul National University
20	ZeNA: Zero-Aware Neural Network Accelerator	
	Dongyoung Kim and Sungjoo Yoo	Seoul National University
21	An Efficient Method to Boosting Performance of Spiking Neural Network Training	
	Seongsik Park, Sungroh Yoon	Seoul National University
22	Near-Data Processing for Differentiable Machine Learning Models	
	Hyukjun Choe and Sungroh Yoon	Seoul National University
23	SplitNet: Learning to Semantically Split Deep Networks for Parameter Reduction and Model Parallelization	
	Sungju Hwang	UNIST
24	Accelerating Neural Network with Selective Thread-Level Parallelism Regulation and Cache Bypassing on GPUs	
	Kwanghee Chang, Yunho Oh, Myung Kuk Yoon, and Won Woo Ro	Yonsei University